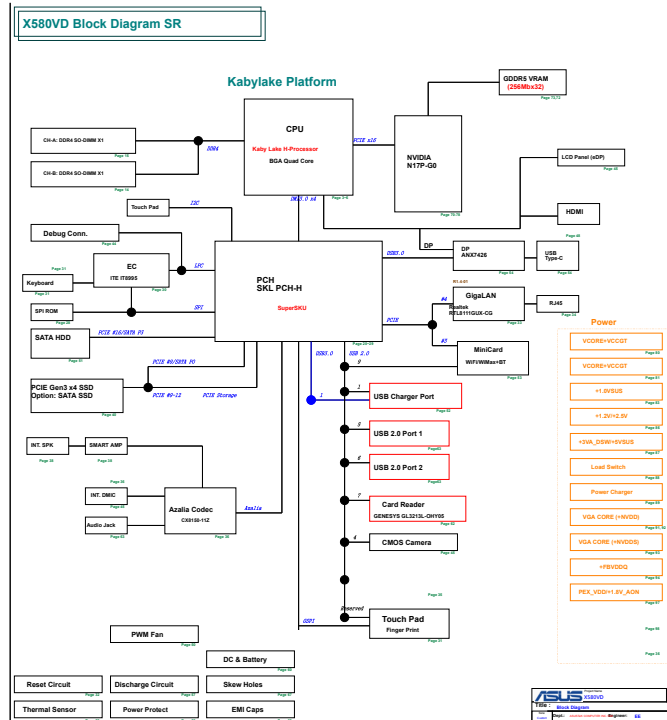


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EC IT2225 GPIO[illegible]

### GLT02VM Setting

[illegible]

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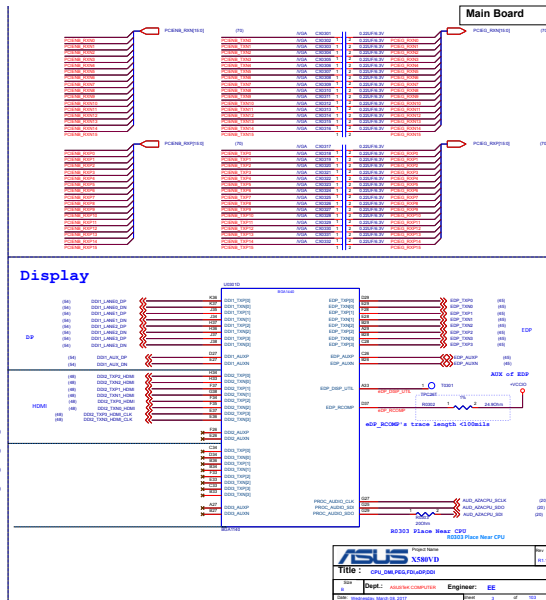
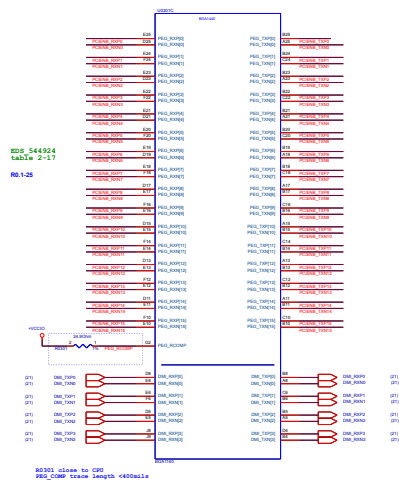
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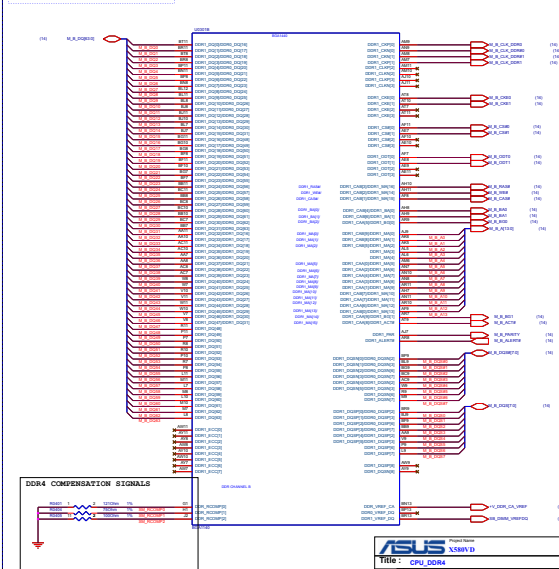
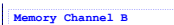
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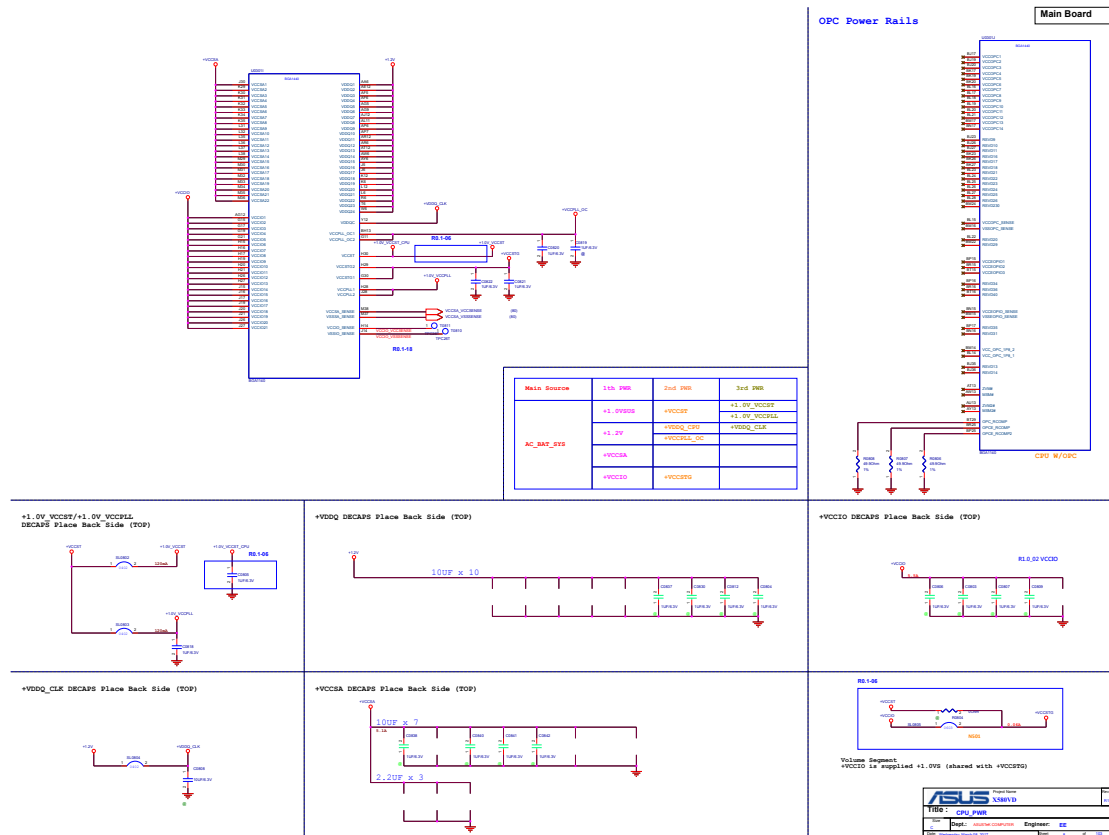




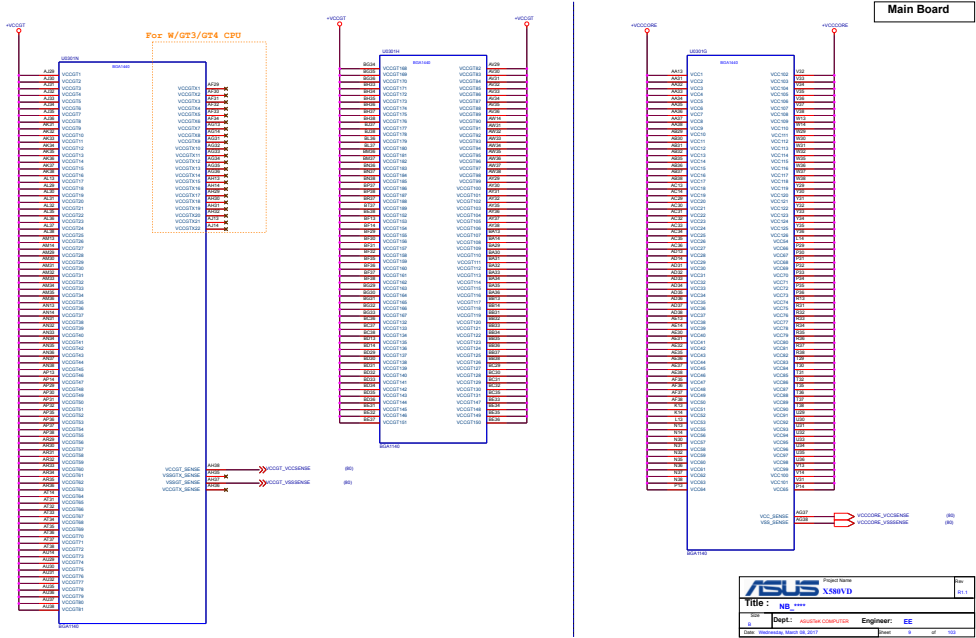






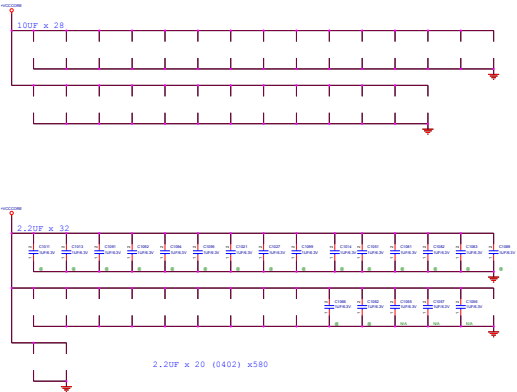




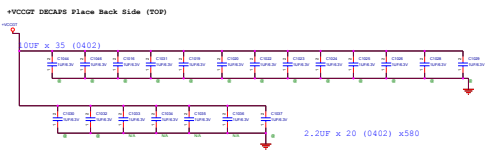




+VCCORE DECAPS Place Back Side (TOP)



+VCCOUT DECAPS Place Back Side (TOP)

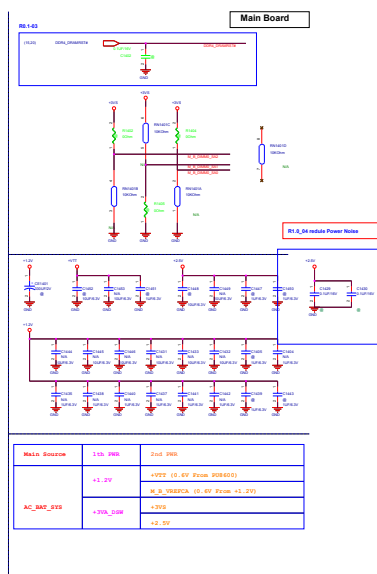
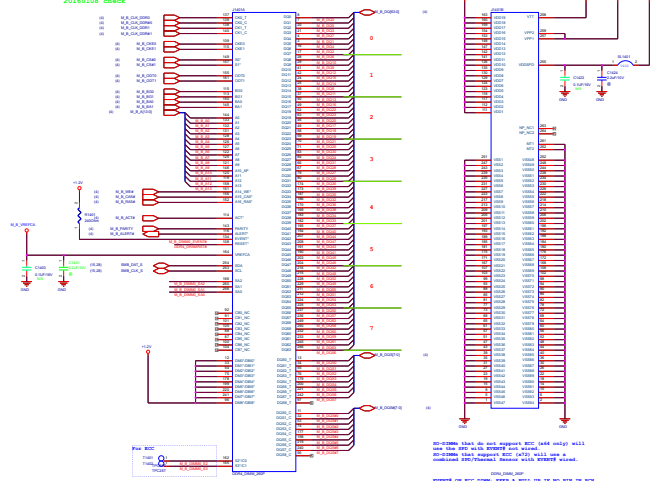


Main Board

ASUS		Rev: 1.0
TBD : 810_CPU_POWER_CAP		
By:	Dpt:	Engineer: ES
Date: 2023-08-08		Page: 1 of 1

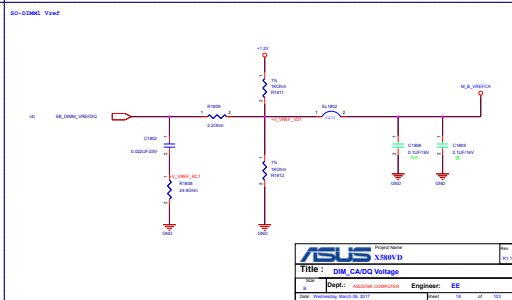
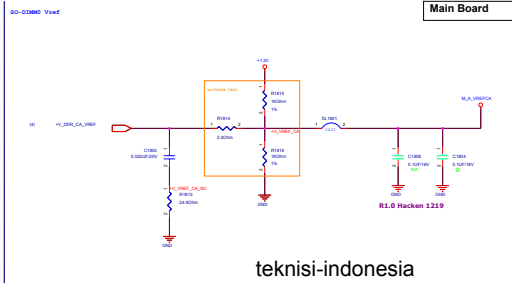


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12002-00080100
DDR4 DIMM 260P 4K STD
20160108 check
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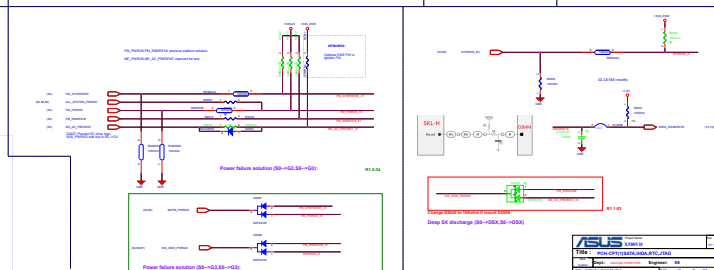
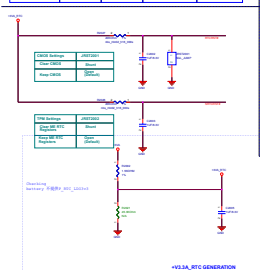
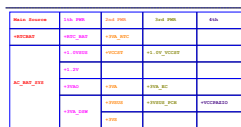


Client Name		Project Name		Date
		X580VD		11/11/2017
Title : DIM_DDR4 SO DIMM A1				
Drawn	Dept.:	ASUS PC COMPUTER INC.	Engineer:	CE



[illegible]





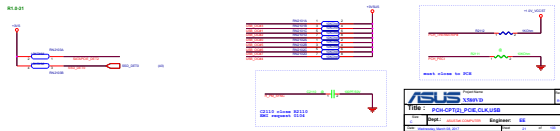
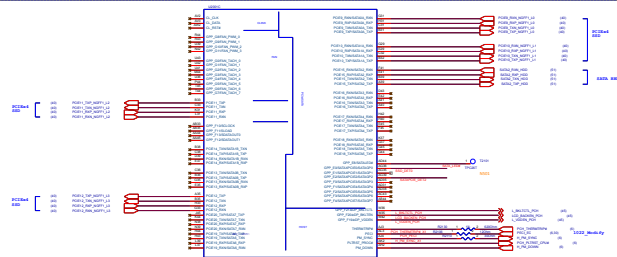
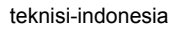


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2	1.002-1	N/A	N/A	Non-SMPT	N/A	N/A
3	1.003-1	N/A	N/A	Non-SMPT	N/A	N/A
4	1.004-1	N/A	N/A	SMPT	N/A	N/A
5	1.005-1	N/A	N/A	Non-SMPT	N/A	N/A
6	1.006-1	N/A	N/A	Non-SMPT	N/A	N/A
7	1.007-1	N/A	N/A	Non-SMPT	N/A	N/A
8	PC-1	N/A	N/A	N/A	N/A	N/A
9	PC-2	N/A	N/A	N/A	N/A	N/A
10	PC-3	N/A	N/A	N/A	N/A	N/A
11	PC-4	N/A	N/A	N/A	N/A	N/A
12	PC-5	N/A	N/A	N/A	N/A	N/A
13	PC-6	N/A	N/A	N/A	N/A	N/A
14	PC-7	N/A	N/A	N/A	N/A	N/A
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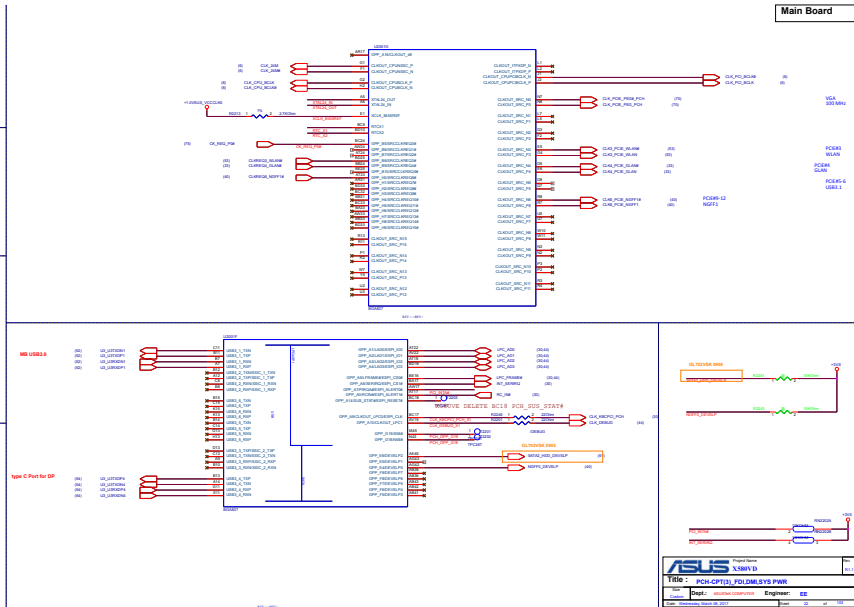
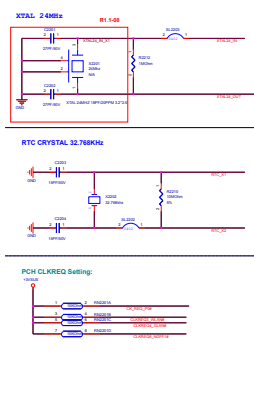
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usb2_02	USB2.0 IO Port2	usb3_02	
usb2_03	TYPE C USB3.0 Port1	usb3_03	
usb2_04	TYPE C USB3.0 Port1	usb3_04	Type C Port for DP
usb2_05		usb3_05	
usb2_06	Camera	usb3_06	
usb2_07	Card reader	usb3_07	
usb2_08	USB2.0 IO Port2	usb3_08	
usb2_09	BT		
usb2_10	FP Finger print		
usb2_11			
usb2_12			
usb2_13			
usb2_14			

	Function		Function
PCI-01	(SATA-7)	PCI-12	(SATA-08)
PCI-02		PCI-14	(SATA-04)
PCI-03	WLAN	PCI-15	(SATA-2)
PCI-06	GLAN	PCI-16	SATA HDD
PCI-05		PCI-19	(SATA-0)
PCI-06		PCI-17	(SATA-0)
PCI-07		PCI-18	(SATA-0)
PCI-08		PCI-19	(SATA-0)
PCI-09	(SATA-04)	PCI-20	(SATA-0)
PCI-10	(SATA-14)		
PCI-11	PCI SSD		
PCI-12	PCI SSD		

	Function
CIARQ-0	DSPU
CIARQ-1	
CIARQ-2	
CIARQ-3	WLAN
CIARQ-8	SIAN
CIARQ-5	
CIARQ-6	PCI SSD
CIARQ-7	
CIARQ-8	
CIARQ-9	
CIARQ-10-15	

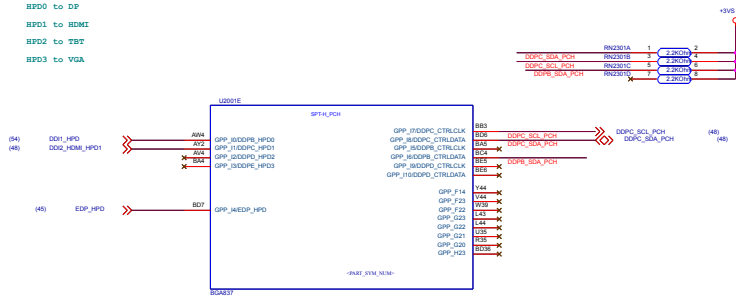






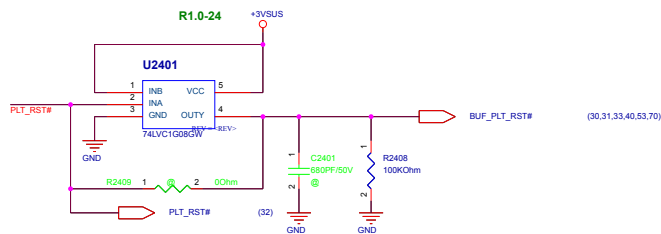
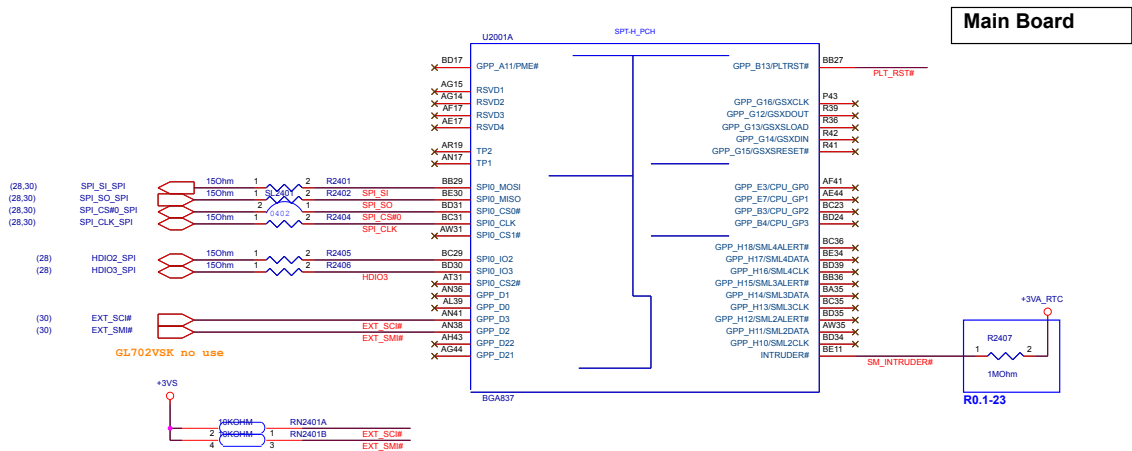


### Main Board


$$\overline{REV} = \langle REV \rangle$$

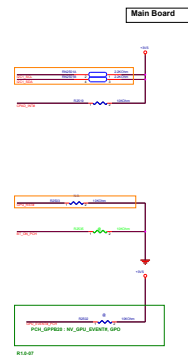
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<b>Title :</b> <b>PCH-CPT(4)_eDP,PCI,DP</b>					
Size <b>A</b>		Dept.: <b>ASUSTek COMPUTER</b>		Engineer: <b>EE</b>	
Date: <b>Wednesday, March 08, 2017</b>		Sheet <b>23</b>		of <b>103</b>	



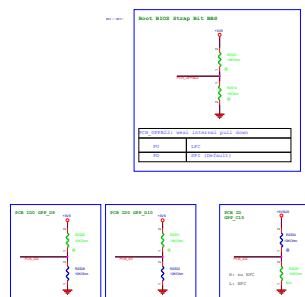


<b>ASUS</b>		Project Name	Rev
X580VD			R1.1
<b>Title :</b> PCH-CPT(5)_LPC,SPI,SMBUS			
Size	Dept.:	ASUSTek COMPUTER	Engineer: EE
A			
Date: Wednesday, March 08, 2017	Sheet	24	of 103

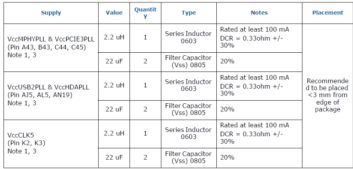




www.teknisi-indonesia.com







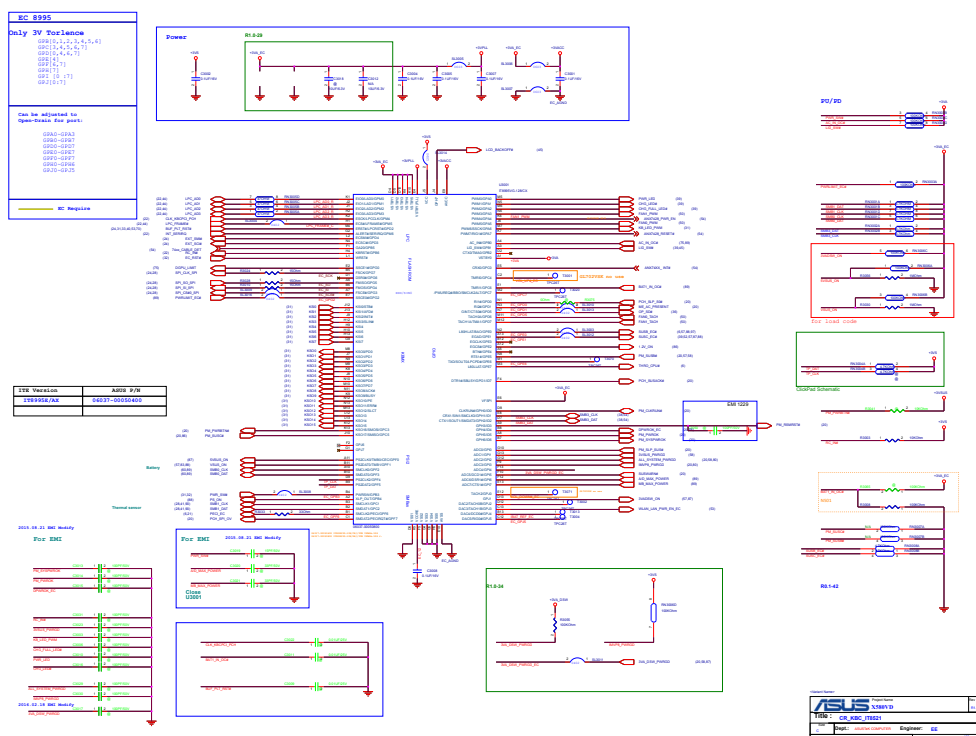




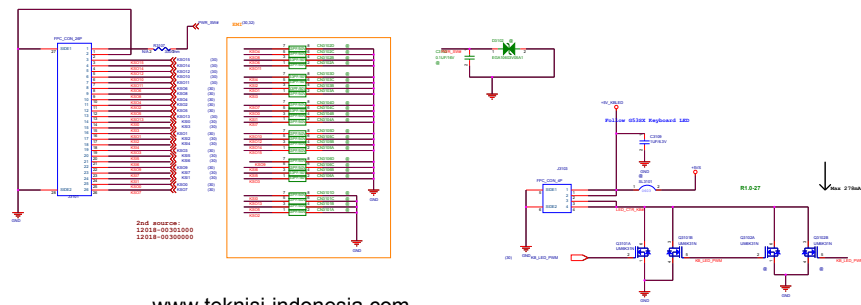






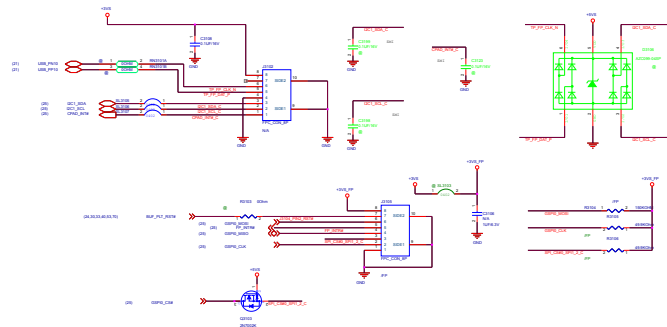






Click touch Pad Connector

Reserved for EMI



ASUS	
Table: HBC KB & TP	
Part:	Engineer:
Rev:	Rev:

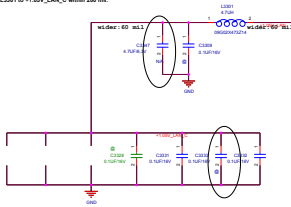


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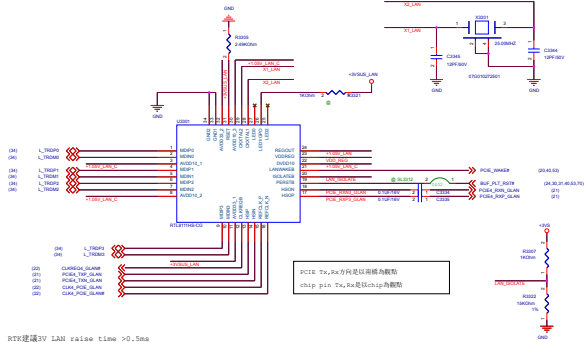
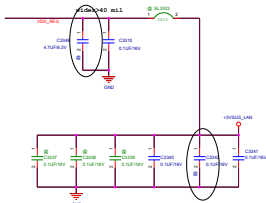
Main Board



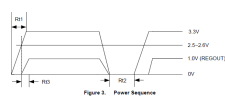
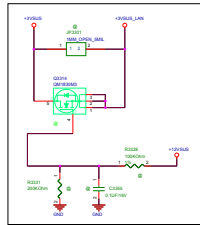
The distance from c3301.36 to L3301 within 200 mil.  
The distance from L3301 to +1.05V\_LAN\_C within 200 mil.



The distance from U3301.54, U3301.55, VDD\_REG net to SL3303 within 200 mil.



8705建議V\_LAN raise time >0.5ms



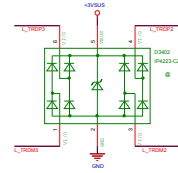
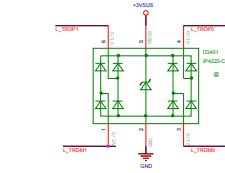
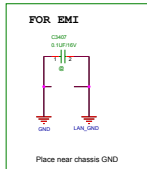
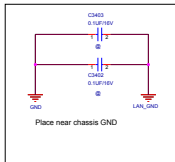
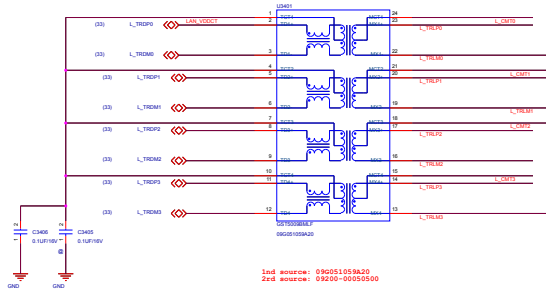
Symbol	Description	Min	Typical	Max	Unit
R1	1.0V Rise Time	10	100	1000	ns
R2	1.0V Fall Time	10	100	1000	ns
R3	1.0V (PREOUT) Settling Time	10	100	1000	ns

Note: See the following section for power sequence requirements.

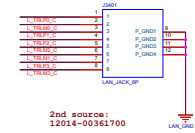
ASUS		Project Name	
Title: LAN_RTL8111		Rev: 1.0	
Drawn: JASON CHEN	Engineer: EE		
Date: 2023/01/10	Sheet: 17	of 100	



## RJ45 con.

R2.2 [Chip]  
Del R3401 for 非繞線式Transformer 1020G

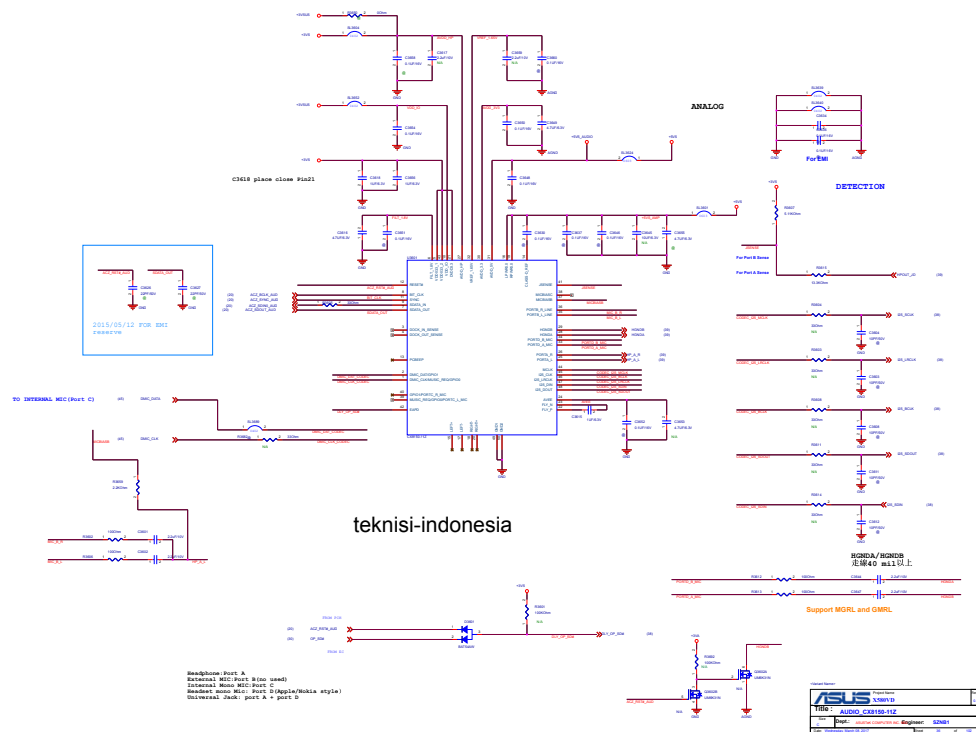
TOP、In1、In2、In2、In3、Bottom、層雲鋪Lan-Gnd



2nd source:  
12014-00361700

		Project Name	Rev
		XS89VD	R1.0
Title : <b>LAN_AR8171_RJ45</b>			
Size	Dept.: <b>AGUSTW COMPUTER INC. Engineer: EE</b>		
8			
Date: Wednesday, March 08, 2017		Sheet 34	of 102



















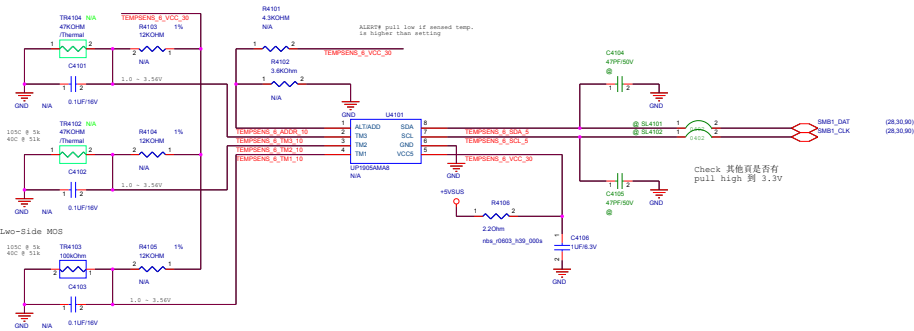
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R4101	10k	1.5k	2k	3.6k	3.9k	4.3k	5.1k	6k
R4102	Open	8.2k	6.2k	6.8k	4.7k	3.6k	2.7k	2k



2nd UP1905  
EE線路for Thermal Team

TR4101 place near DGPU VRAM ,  
SSD base on thermal RD test

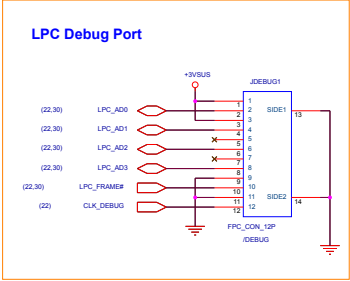
TR4102 place near KBL CPU

TR4103 place near DGPU VRM Lwo-Side MOS



		Project Name		Rev
				1.0
<b>Title :</b> UP1905_Thermal_IC				
Size	Dept.:	Engineer:		
Custom	NB2-AD-1E256c1	Evelyn Weng		
Date: Wednesday, March 08, 2017	Sheet	41	of	103

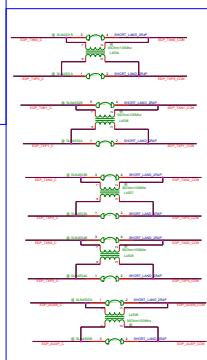
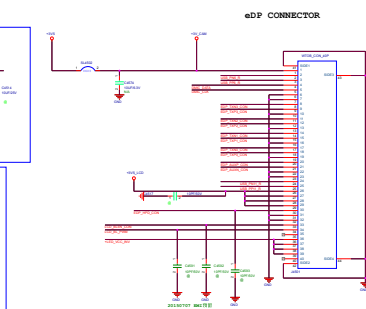
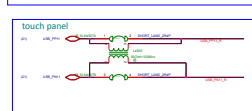
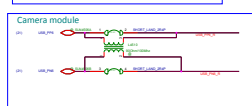
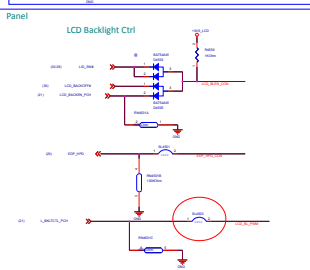
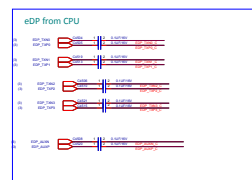
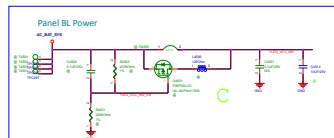
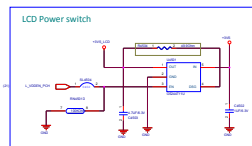




<Variant Name>

<b>ASUS</b>		Project Name	Rev
		<b>X580VD</b>	Rev 1
Title : <b>DEBUG_LPC</b>			
Size	Dept.:	ASUSTeK COMPUTER INC.	Engineer: EE
A			
Date: Wednesday, March 08, 2017		Sheet	44 of 103





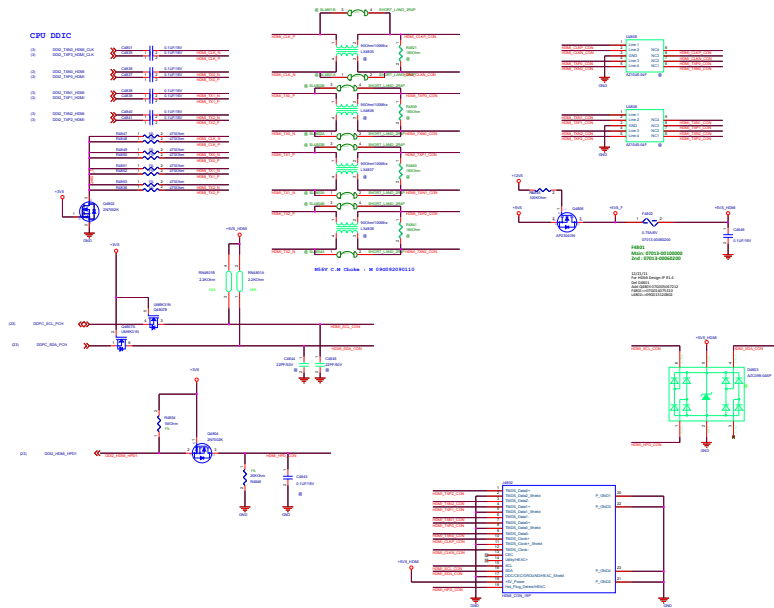


eDP to VGA

CRT D-SUB



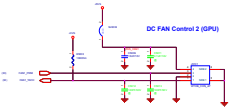
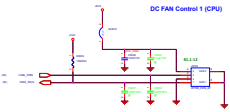
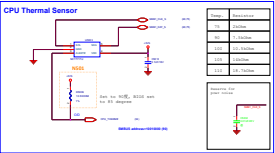




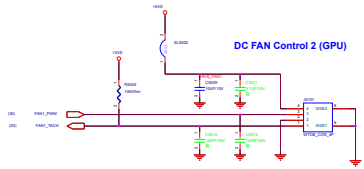
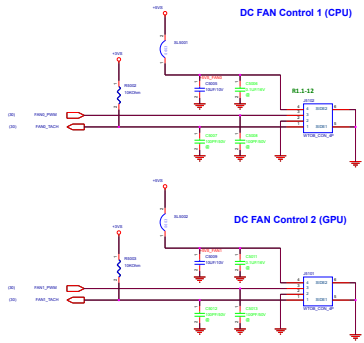
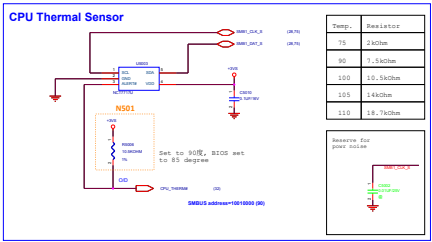
MS4V C.M. Choke : M 090092090110

		Project Name		Rev	
		XS80VD		R1.1	
Title : HDMI					
Date		Dept:		Engineer:	
C		ASUSTeK COMPUTER INC.		EE	
Date: Wednesday, March 08, 2017		Sheet		of 100	

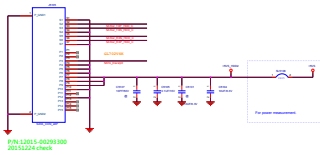




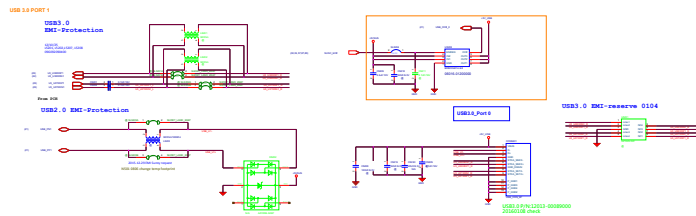












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ASUS

Model: USB Charger

Title: USB Charger

Input: 5V 2.1A

Output: 5V 2.1A

Power: 10.5W

Material: Plastic

Color: Black

Weight: 100g

Dimensions: 100x50x20mm

Package: 1x USB Charger

Price: \$19.99

Availability: In Stock

Shipping: Free Shipping

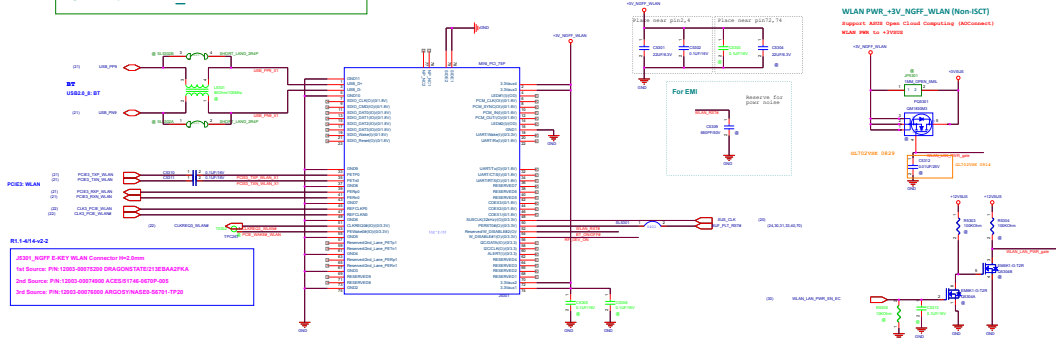
Warranty: 1 Year

ASUS



RL1-02

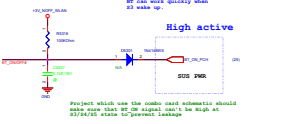
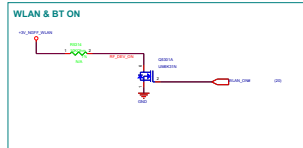
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WLAN\_WAKE#

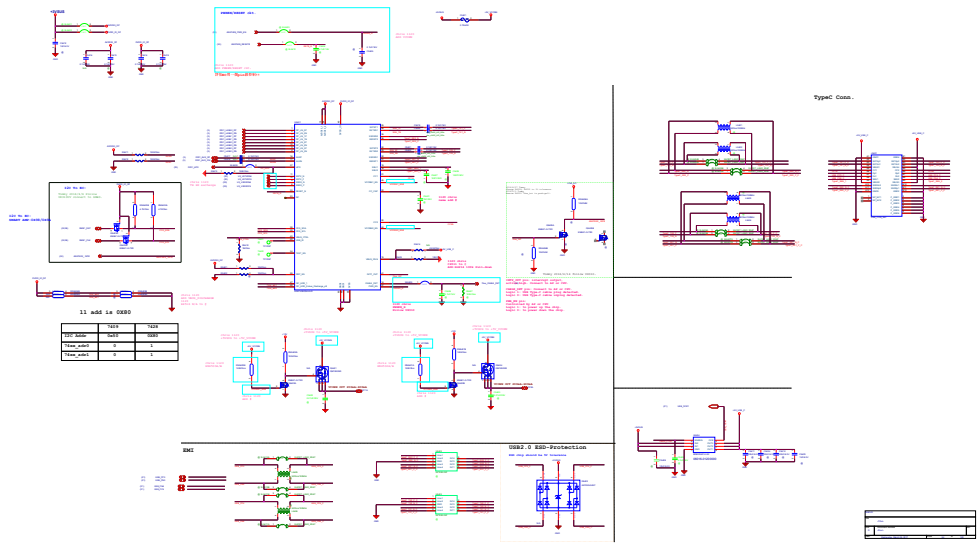
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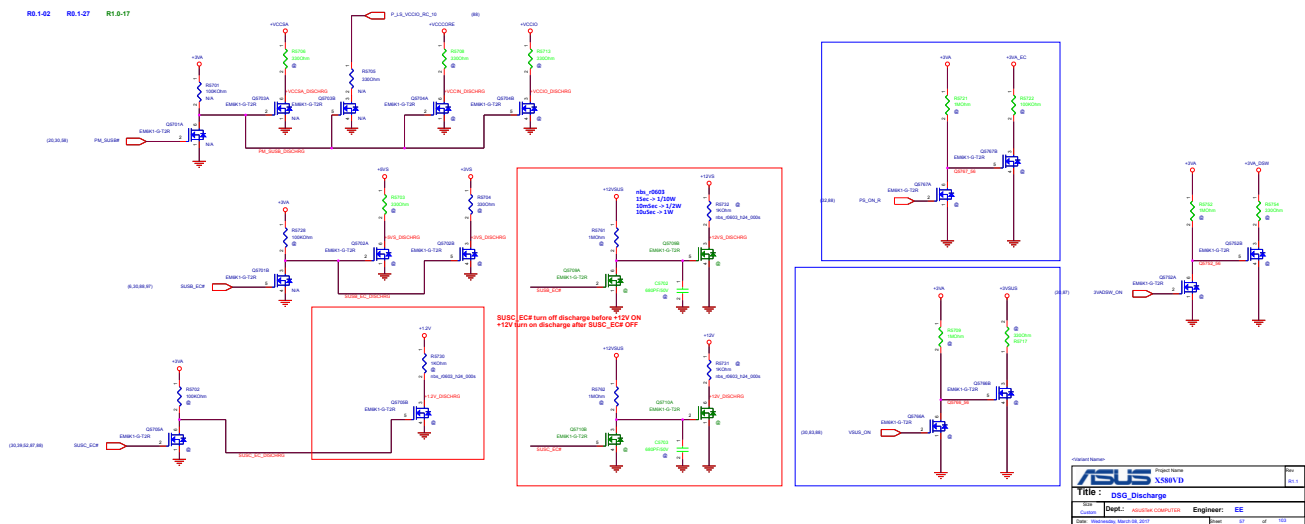
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ASUS	
Model: WFIWIMax	
Part No.	WFIWIMax
Rev.	1.0
Page	1 of 1







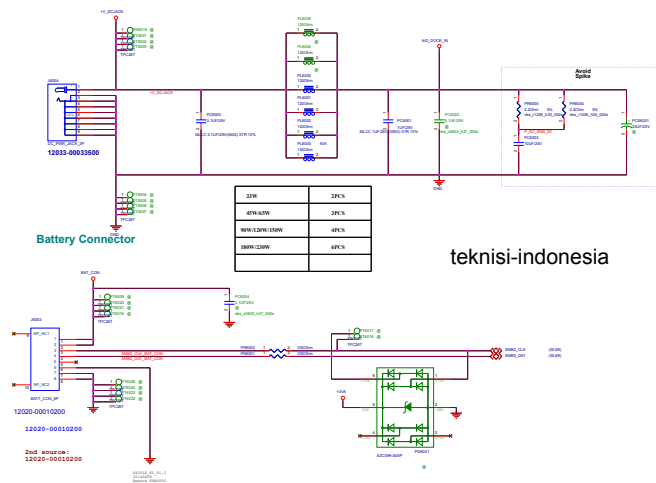






*Variant Name*		Project Name		Rev
		X580VD		Rev. 1
Title : <b>Power Protect</b>				
Size	Dept.: <b>ASUSTek COMPUTER</b>		Engineer:	<b>EE</b>
Custom				
Date: Wednesday, March 08, 2017		Sheet	58	of 103

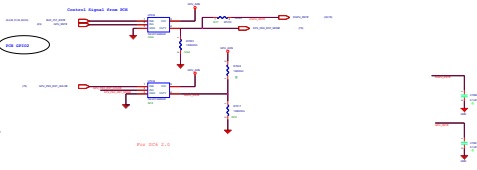
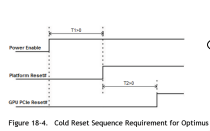
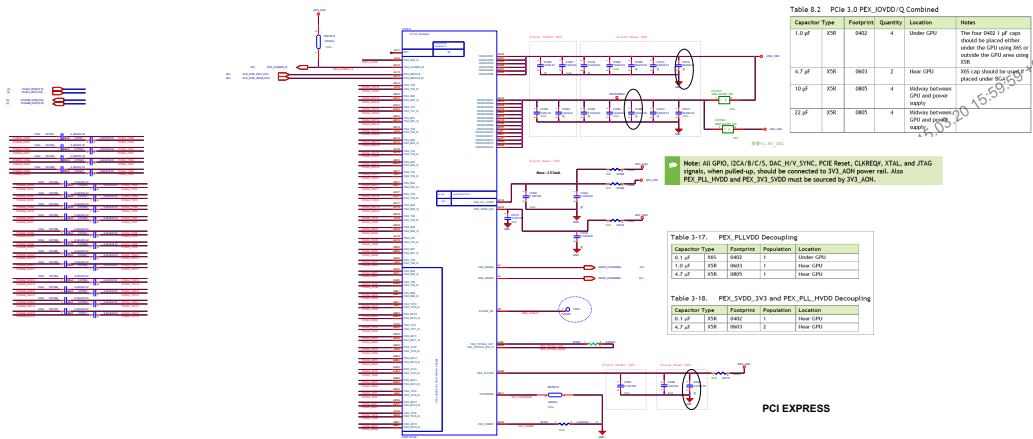




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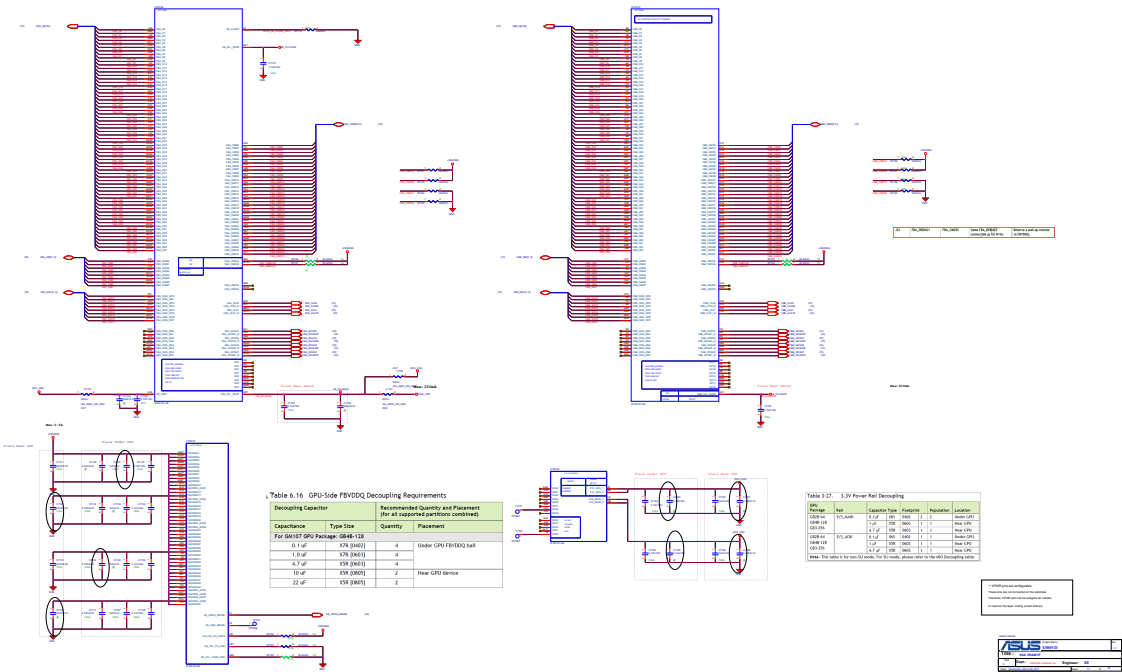
ASUS X580VD	
Title: X580VD Battery Connector	
Date: 2023-08-01	
Author: teknisi-indonesia	
Version: 1.0	





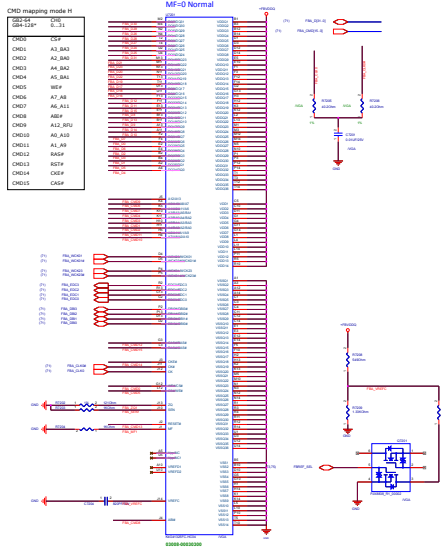


GPU MEMORY INTERFACE: PARTITION A





## FBA Partition Memory (1 of 2)



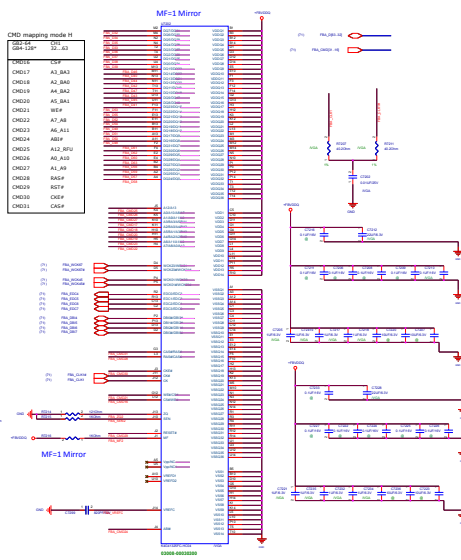
	PG331
+1.20V	8.06K
+1.20V	10K00

USE GDDR5 VRAM 128MB x 32 (512MB)

14E: P/N: 03008-00030900 HYUNDAI-GC-6H2AAJR-T2C(A-4se) ,Strap: 6w6 (+1.35V)

2nd: PIN: 02008-00020200 SAMSUNG-KieG41225FC-HC84 ,Strap: 8x3 (+1.5V, non-RVL)

## FBA Partition Memory (2 of 2)

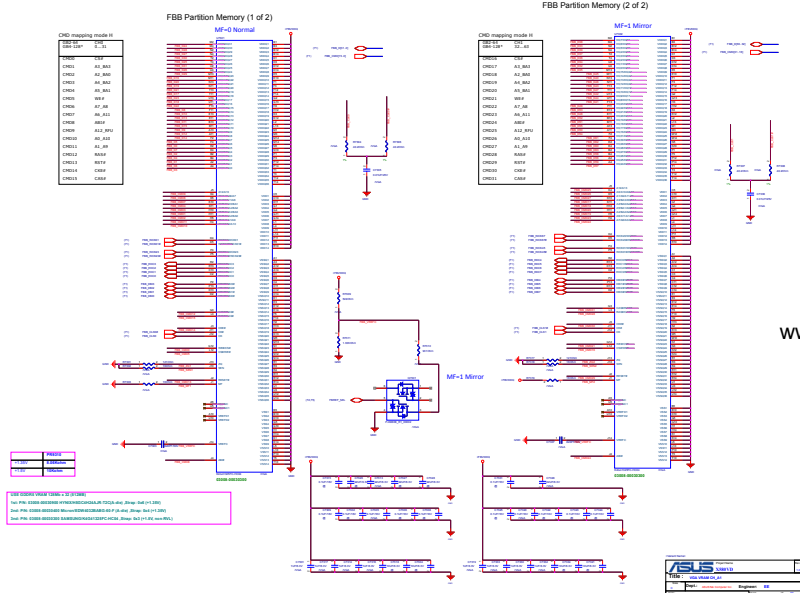


## GDD5 MODE SELECTION

MODE	HF	EBCL	EBCL
x16	I	O	ICDQ
x32	I	VSDQ	ICDQ
x16 mirrored	ICDQ	VSDQ	I
x32 mirrored	ICDQ	VSDQ	ICDQ









CRT DAC\_A



DP(link C)



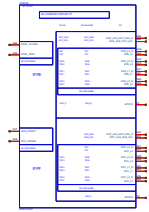
DVI(link D)



LVDS IFPA/B



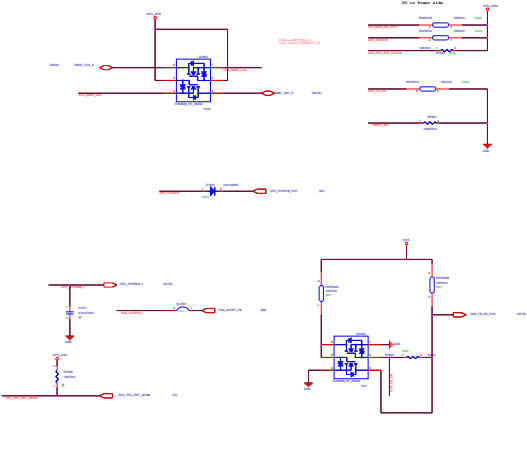
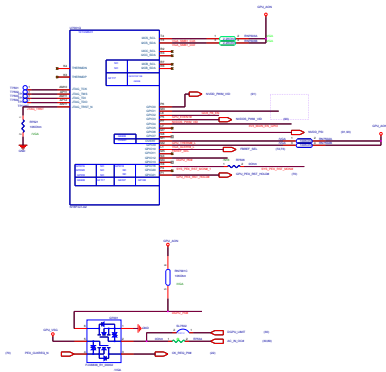
LVDS IFPE/F





GPIO, TEMP SENSOR, JTAG  
I2C ADDRESS: 0d60h

For SOC 2.0



GPIO Pin	N16 GPU Function	N16 GPU Function on Co-Design	N17 GPU Function	Comments
GPIO0	GCL_FB_EN	PWM_VID	PWM_VID	N17 PWM_VID for NVVDD supply
GPIO1	HEX_VDD_CTL	GCL_FB_EN	GCL_FB_EN	
GPIO2	LCD_BL_PWM	GPU_EVENT0	GPU_EVENT0	
GPIO3	LCD_VCC	TBD	INVDD0_PWM	N17 PWM_VID for INVDD0 supply
GPIO4	LCD_BLEN	SVL_MAIN_EN	TVL_MAIN_EN	
GPIO5	SVL_MAIN_EN	FRAME_LOCKM	FRAME_LOCKM	
GPIO6	GPU_EVENT1	PS	PS	N17 PS for HPPD supply
GPIO7	3D_VISION	LCD_BL_PWM	LCD_BL_PWM	
GPIO8	SYS_RST_RST_AND_MF	BPFE_IPFD	BPFE_IPFD	
GPIO9	THERM_ALERT	THERM_ALERT	THERM_ALERT	Same
GPIO10	HEX_VDD_CTL	HEX_VDD_CTL	HEX_VDD_CTL	Same
GPIO11	PWM_VID	LCD_VDD	LCD_VDD	Same
GPIO12	PWM_LEVEL	PWM_LEVEL	PWM_LEVEL	Same
GPIO13	PS	LCD_BLEN	LCD_BLEN	Same
GPIO14	HPO_IPFA	HPO_IPFA	HPO_IPFA	Same
GPIO15	HPO_IPFC	HPO_IPFB	HPO_IPFB	Same
GPIO16	FRAME_LOCKM	GPU_RST_RST_AND_MF	GPU_RST_RST_AND_MF	
GPIO17	HPO_IPFD	HPO_IPFD	HPO_IPFD	Same
GPIO18	HPO_IPFE	HPO_IPFE	HPO_IPFE	Same
GPIO19	HPO_IPFB	3D_VISION	3D_VISION	
GPIO20	RESERVED	RESERVED	RESERVED	
GPIO21	GPU_RST_RST_AND_MF	GPU_RST_RST_AND_MF	GPU_RST_RST_AND_MF	
GPIO22	RESERVED	RESERVED	RESERVED	
GPIO23	RESERVED	RESERVED	RESERVED	New N17 GPIO
GPIO24	RESERVED	RESERVED	RESERVED	New N17 GPIO
GPIO25	RESERVED	RESERVED	RESERVED	New N17 GPIO
GPIO26	RESERVED	RESERVED	RESERVED	New N17 GPIO
GPIO27	RESERVED	RESERVED	RESERVED	New N17 GPIO



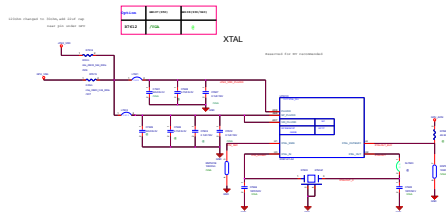
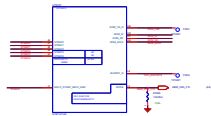


Table 3. N17P-G0/G1 GDDR5 Recommended Memories

Memory Density	Memory Configuration	Manufacturer	Manufacturer Part Number	Die Revision	Strap	Memory Speed Grade	Data Code	Qual Plan	Status
8 Gb	256Mb x32	Micron	MT51J2GMA2DF-70A	B-die	Dx1	7 Gbps	N/A	Full	Production ready
			H5GCB24MUR-R0C	A-die	Dx2	7 Gbps	N/A	Full	Post production ready
4 Gb	128Mb x32	Samsung	K4G4132FE-HC38	E-die	Dx1	7 Gbps	N/A	Full	Production ready
		Hynix	H5GCB24MUR-R0C	D-die	Dx2	7 Gbps	N/A	Full	Production ready
	1.25V and 1.35V	Micron	EDW4132ASG-104	A-die	Dx1	7 Gbps	N/A	Full	Post production ready

Note:  
1. For N17P-G0, the maximum allowable memory case temperature is 85 °C.  
2. N17P-G0 uses NCL30 up to 1000MHz (G1: 1000 - 1.25V, G0: 1000 up to 1000 MHz).

Table 7. N17P-G0/G1 GDDR5 Recommended Memories

Memory Type	Memory Density	Manufacturer	Manufacturer Part Number	Die Revision	Strap	Memory Speed Grade	Memory Data Code	Memory Status
GDDR5	128Mb x16	Hynix	H5GCB24MUR-R0C	B-die	Dx1	7 Gbps	N/A	Production ready
		Micron	E5H4132FE-70A	B-die	Dx1	7 Gbps	N/A	Production ready
		Samsung	K4G4132FE-70A	E-die	Dx1	7 Gbps	N/A	Production ready
	256Mb x16	Hynix	H5GCB24MUR-R0C	D-die	Dx2	7 Gbps	N/A	Post production ready
		Micron	E5H4132FE-70A	B-die	Dx1	7 Gbps	N/A	Production ready
		Samsung	K4G4132FE-70A	E-die	Dx1	7 Gbps	N/A	Production ready
GDDR5	1.25V	Hynix	H5GCB24MUR-R0C	D-die	Dx2	7 Gbps	N/A	Post production ready
		Micron	E5H4132FE-70A	B-die	Dx1	7 Gbps	N/A	Production ready
		Samsung	K4G4132FE-70A	E-die	Dx1	7 Gbps	N/A	Production ready

Note:  
1. For N17P-G0/G1, the maximum allowable memory case temperature is 85 °C.  
2. G0 is supported in 12 configurations, G1 is supported in 12 configurations.

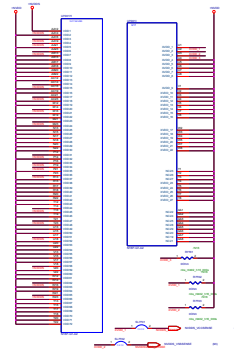
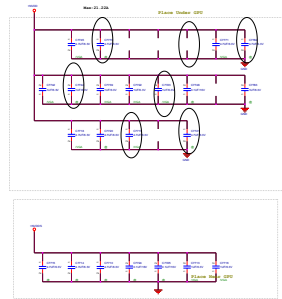




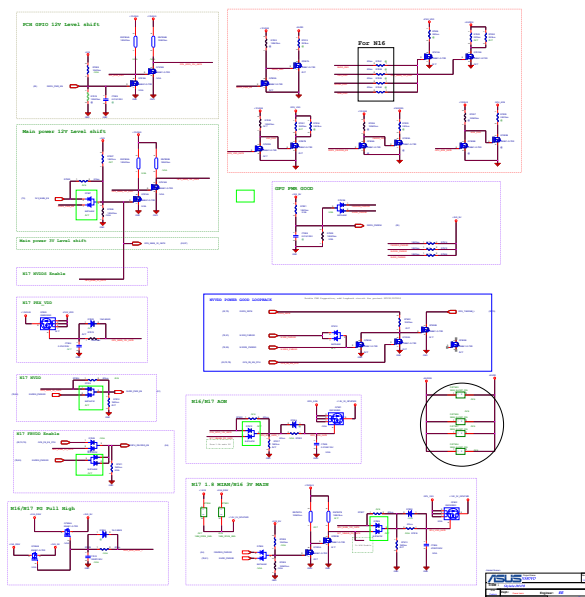
# NVVDD GROUND



# NVVDD POWER AND DECOUPLING

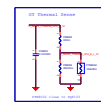
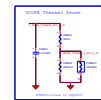
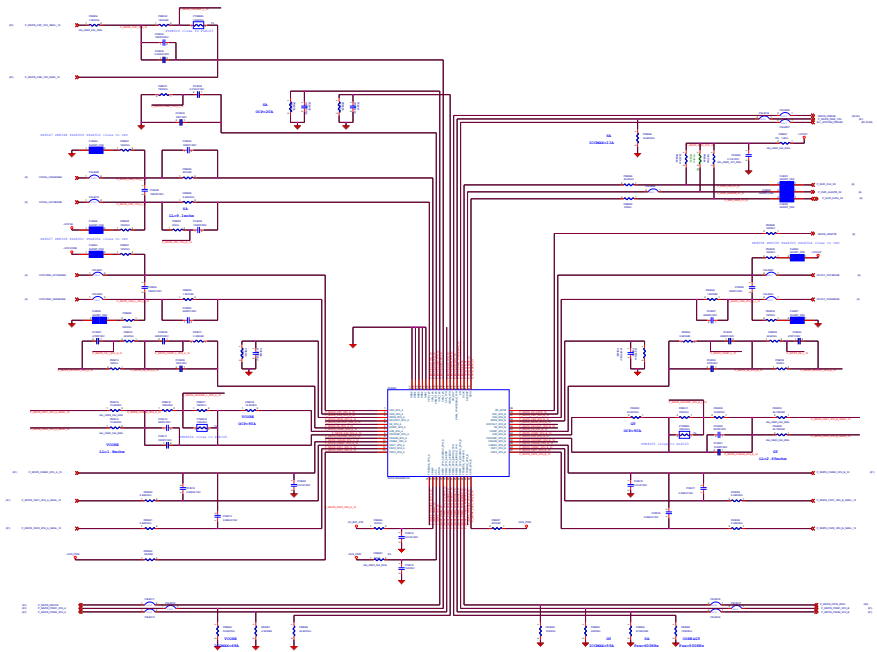






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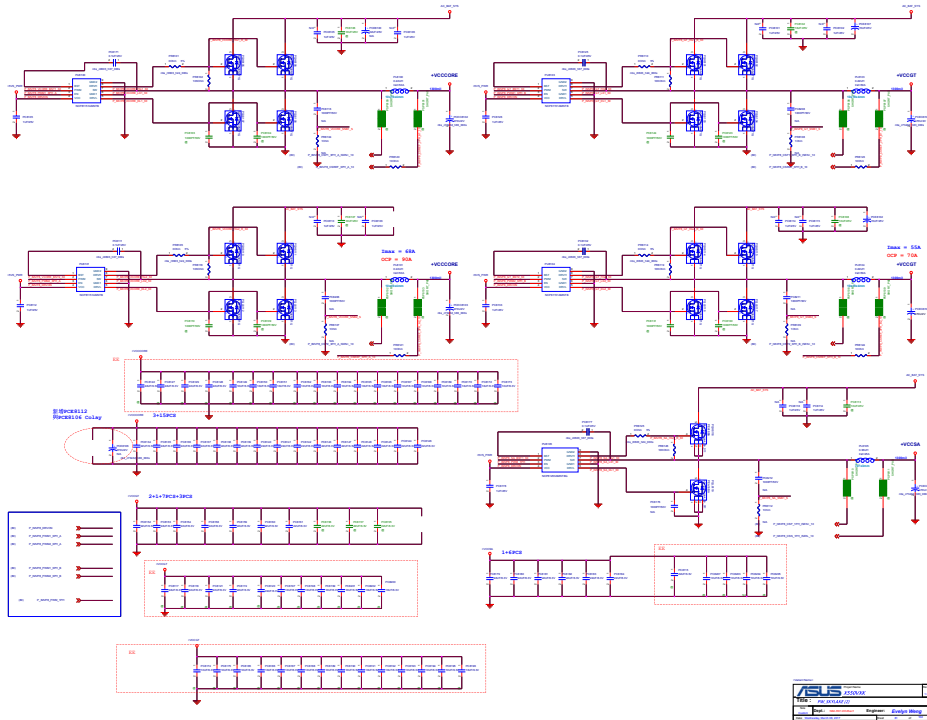




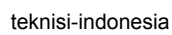
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ASUS	ASUS



# Kabelake IMVP8 Power (For CPU)

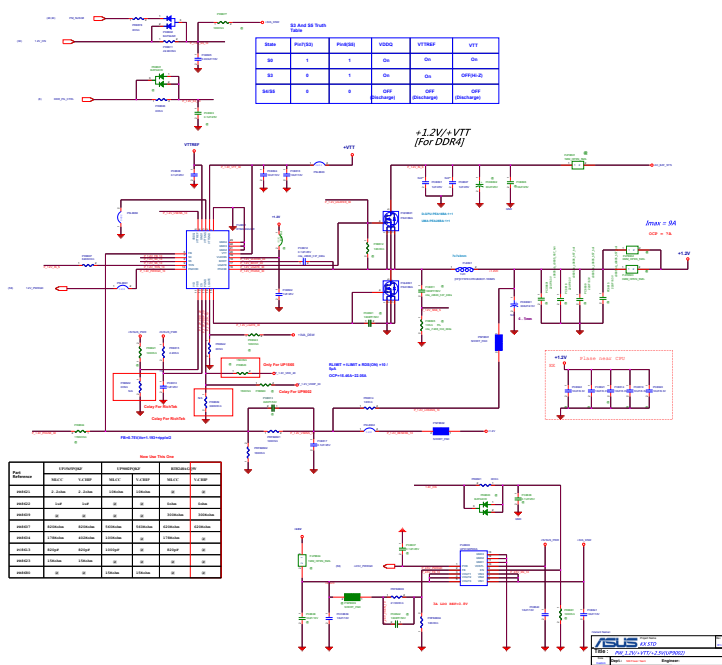




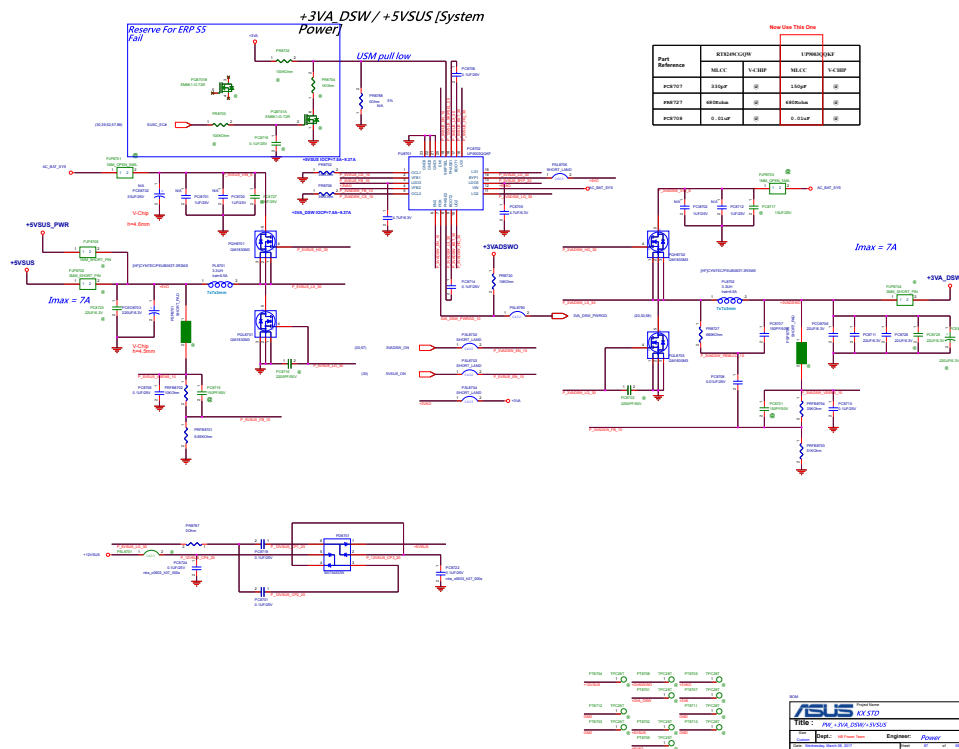


Part Reference	17F0000001		17F0000002		REVISION	
	MLCC	V-CRIP	MLCC	V-CRIP	MLCC	V-CRIP
PR0234	2.2ohm	2.2ohm	100ohm			
PR0300	1uF					
PR0337					200ohm	200ohm
PR0338	820ohm	820ohm	150ohm	150ohm	420ohm	420ohm
PR0341	170ohm	820ohm	100ohm		170ohm	
PR0315	820uF	800uF	100uF		800uF	
PR0301	150ohm	150ohm				
PR0302			150ohm	150ohm		





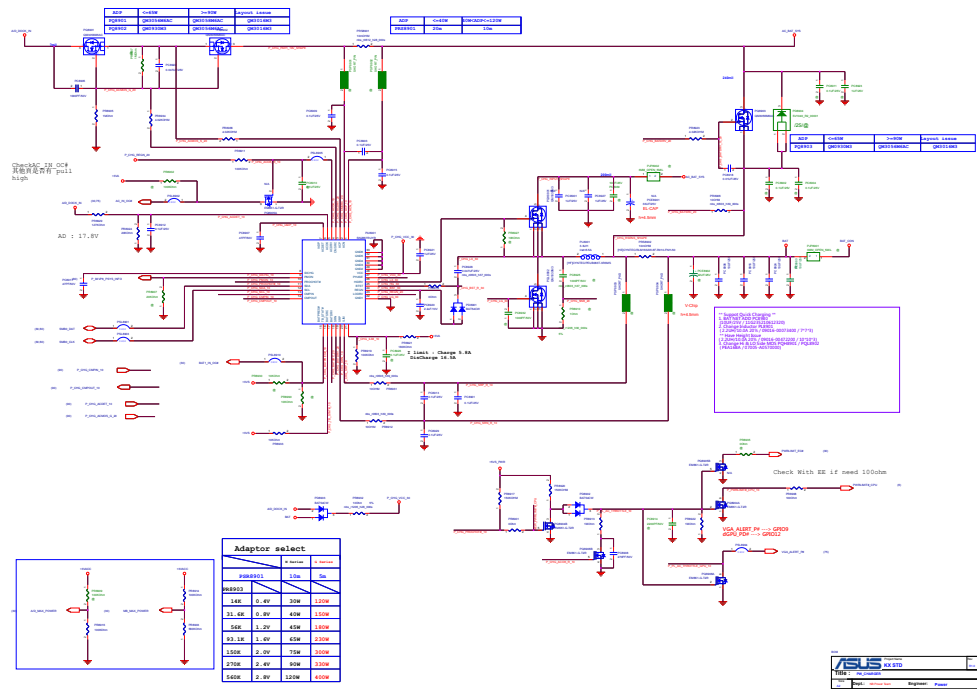




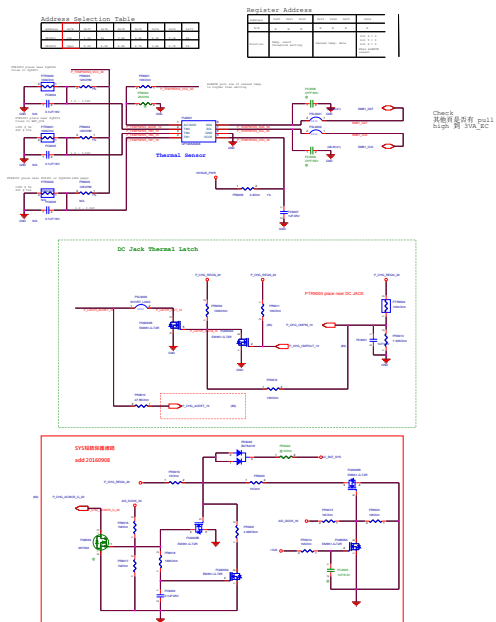








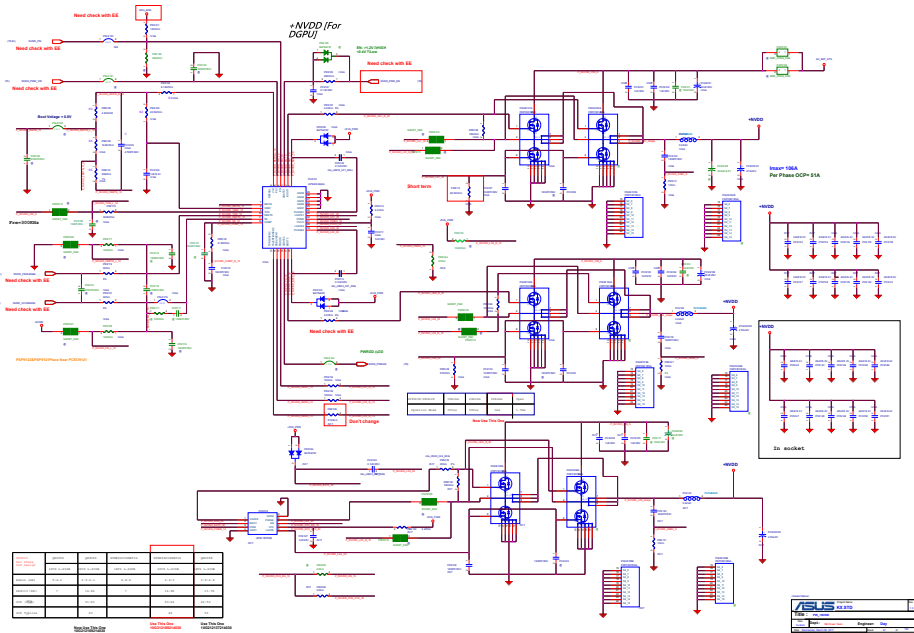




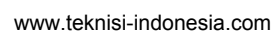
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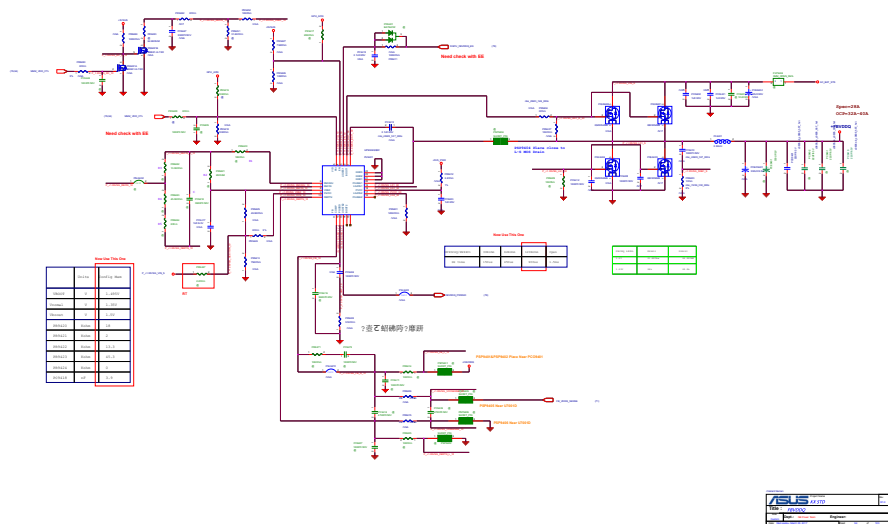




REVENUE AND EXPENSE	2015		2016	
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REVENUE 2015	51.000	42.000,00		
REVENUE 2016	60	50		
REVENUE 2017	60	50		
EXPENSE 2015	10.000,00	8.000,00		
EXPENSE 2016	60	50		
EXPENSE 2017	60	50		

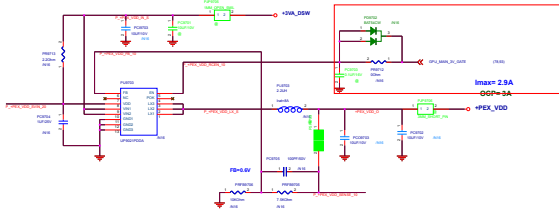






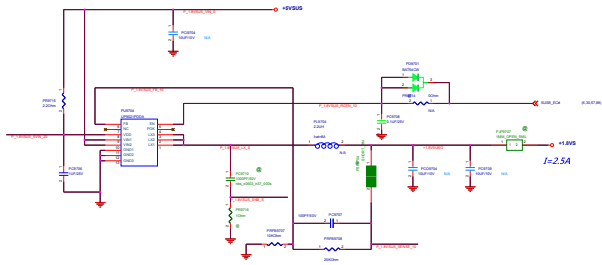


## N17 POWER quence



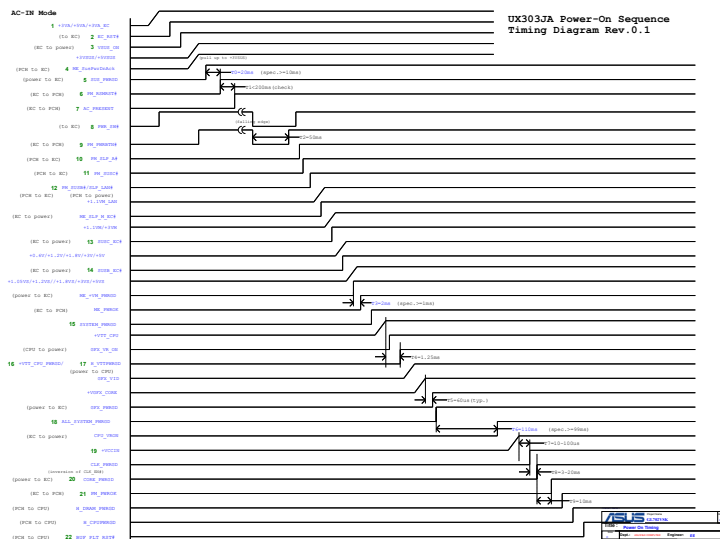
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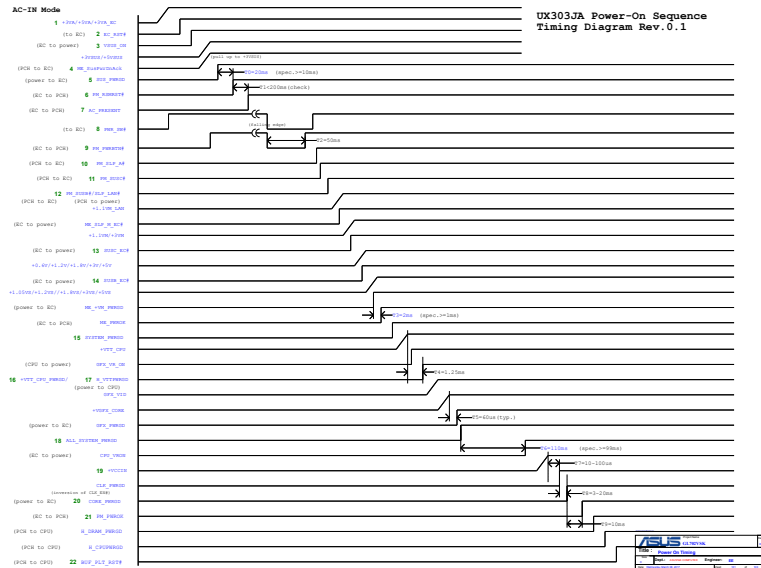


 Project Name: <b>KX STD</b>		Rev: <b>00.01</b>
<b>Title:</b> <b>POWER_VVFX_CORE</b>		
Date: <b>01/08/2017</b> Created by: <b>EE</b>	Dept: <b>ASUS Taiwan COMPUTEX</b>	Engineer: <b>EE</b>
Date: <b>Wednesday, March 08, 2017</b>	Issue: <b>001</b>	of: <b>001</b>











[illegible]